IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jae-Yong Jeong and Sung-Soo Lee

Serial No.:

not yet assigned

Group Art Unit:

not yet assigned

Filed:

September 9, 2003

Examiner:

not yet assigned

For:

NONVOLATILE SEMICONDUCTOR MEMORY WITH A

PROGRAMMING OPERATION AND THE METHOD THEREOF

Mail Stop Patent Applications Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant submits herewith PTO Form 1449 listing patents and publications of which he is aware which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR Section 1.56.

The above-referenced application is a continuation of U.S. Serial No. 10/021,639, filed December 12, 2001 and is now pending.

It includes the same disclosure as U.S. patent application Serial No. 10/021,639

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application. 37 CFR Section 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

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Respectfully submitted,

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			MATION DISC FORM PTO-1				
U.S. PATENT DOCUMENTS							
Exam <u>Init</u>	Ref	Document <u>Number</u>	Issue <u>Date</u>	<u>Name</u>		Class	Sub <u>Class</u>
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OTHER DOCUMENTS							
Exam Init Ref Author, Title, Date, Pertinent Pages, Etc.)							
Examiner	:						
Date Cons	sidered:						